

600V 35.5mohm Super-Junction Power MOSFET
AK3S60N355TMF

Description:

This SJ device provides good FOM performance, better EMI for customer application.

Features:

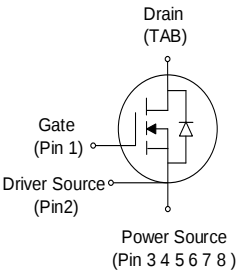
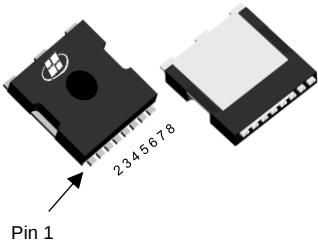
- EMI-Friendly
- RoHS compliant
- Halogen-free
- Wettable Flank for Improved Optical Inspection

Applications:

- High Frequency Switching
- High Efficiency SMPS

Key Performance Parameters:

Parameter	Value	Unit
V_{DS}	600	V
$R_{DS(on), max} @V_{GS} = 10\text{ V}$	35.5	mΩ
I_D	63	A



Ordering Information:

Ordering Code	Package Type	Marking Code	Form	Packing
AK3S60N355TMF	TOLL	3S60N355TMF	Tape Reel	2000 per reel

Maximum Ratings (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Value	Units
V _{DS}	Drain - Source Voltage	600	V
I _D	Drain Current - Continuous (T _C = 25°C) ^(Note 1)	63	A
	Drain Current - Continuous (T _C = 100°C)	40	A
I _{DM}	Drain Current - Pulsed ^(Note 2)	200	A
V _{GS}	Gate - Source Voltage	± 30	V
E _{AS}	Single Pulsed Avalanche Energy ^(Note 3)	595	mJ
P _D	Power Dissipation (T _C = 25°C)	357	W
dV/dT	MOSFET dv/dt ruggedness, V _{DS} = 0...400 V	120	V/ns
	Reverse diode dv/dt, V _{DS} = 0...400 V	70	V/ns
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C

Thermal Characteristics

Symbol	Parameter	Value	Units
R _{θJC}	Thermal Resistance, Junction - to - Case, Steady State	0.35	°C/W
R _{θJA}	Thermal Resistance, Junction - to - Ambient, Steady State ^(Note 4)	45	°C/W

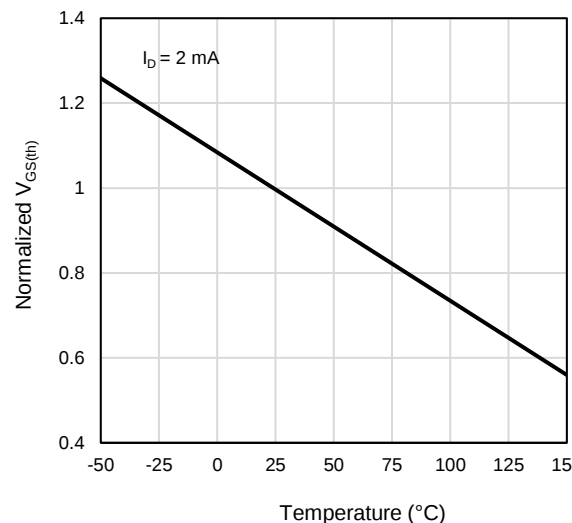
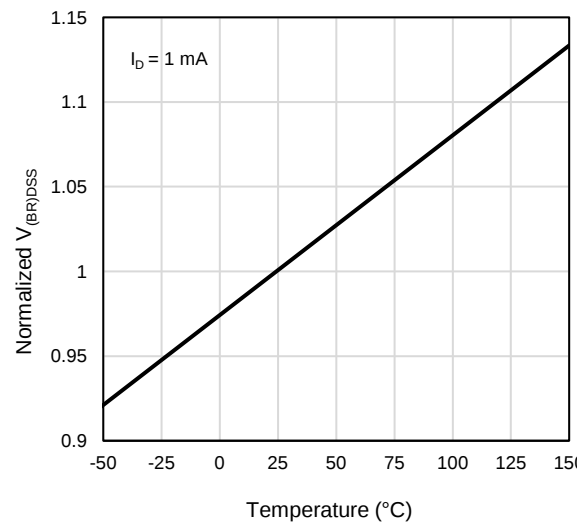
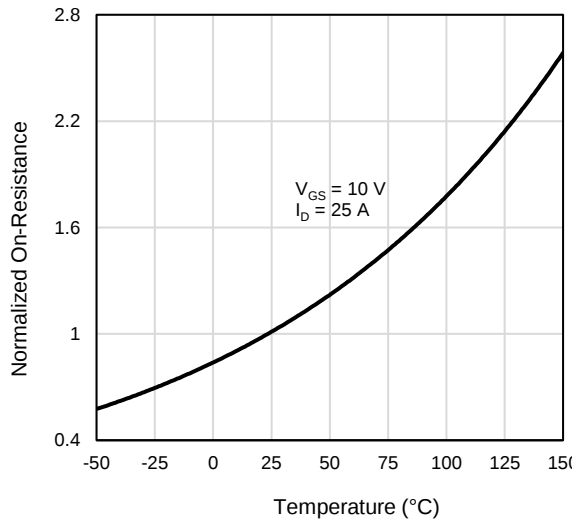
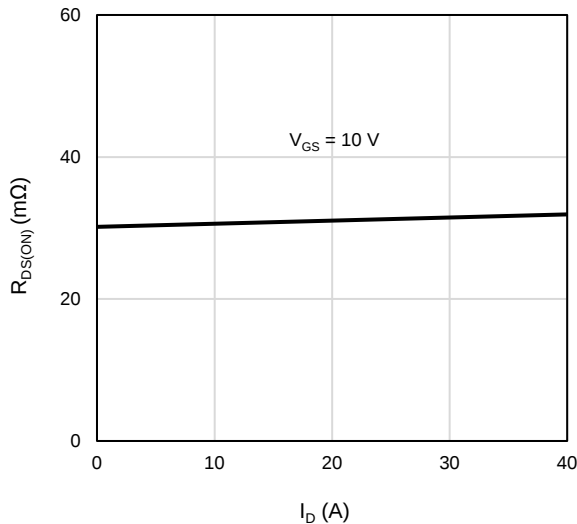
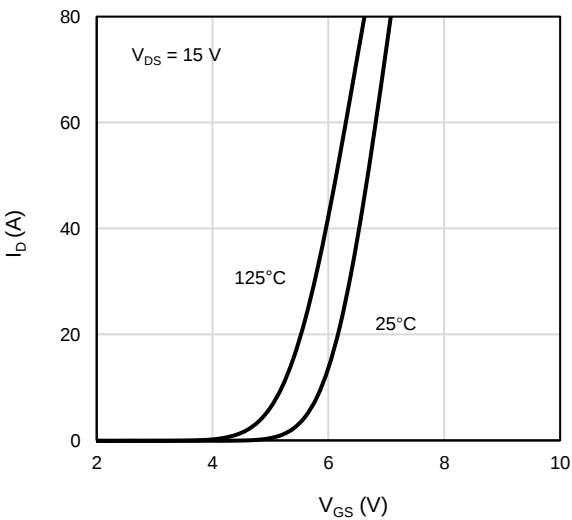
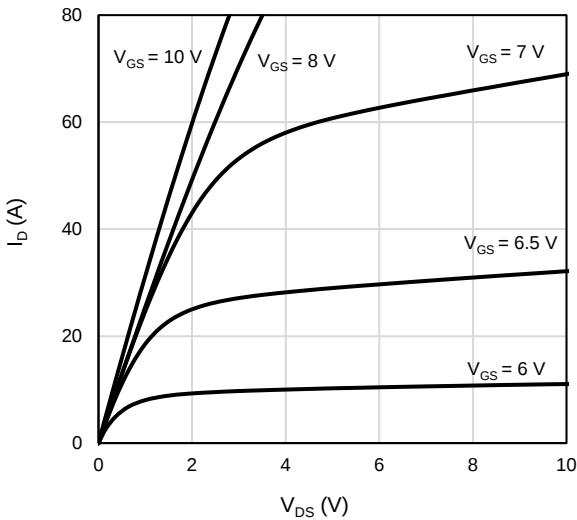
Notes:

- 1. The max drain current rating limited by maximum junction temperature
- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3. L = 10.8 mH, V_{DD} = 100 V, I_{AS} = 10.5 A, R_G = 50 Ω, Starting T_J = 25 °C
- 4. Mount on minimum PCB layout

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Static Characteristics						
V _{(BR)DSS}	Drain - Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA	600			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V			10	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 30 V, V _{DS} = 0 V			±100	nA
V _{GS(th)}	Gate Threshold voltage	V _{DS} = V _{GS} , I _D = 2 mA	3	4	5	V
R _{DS(ON)}	Drain - Source on - state resistance	V _{GS} = 10 V, I _D = 25 A		32	35.5	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0 V, f = 1 MHz		3975		pF
C _{oss}	Output Capacitance			90		pF
C _{rss}	Reverse Transfer Capacitance			33		pF
C _{o(er)}	Effective output capacitance, energy related	V _{DS} = 0...400 V, V _{GS} = 0 V		177		pF
C _{o(tr)}	Effective output capacitance, time related	V _{DS} = 0...400 V, V _{GS} = 0 V, I _D =constant		1400		pF
R _g	Gate Resistance	f = 1 MHz		4		Ω
Switching Characteristics						
t _{d(on)}	Turn On Delay Time	V _{DD} = 400 V , I _D = 16 A, V _{GS} = 10 V, R _G = 3 Ω		105		ns
t _r	Rise Time			42		ns
t _{d(off)}	Turn Off Delay Time			120		ns
t _f	Fall Time			8		ns
Q _g	Total Gate Charge	V _{DD} = 400 V , I _D = 16 A, V _{GS} = 10 V		136		nC
Q _{gs}	Gate - Source Charge			22		nC
Q _{gd}	Gate - Drain Charge			71		nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Body - Diode Forward Current				63	A
I _{SM}	Maximum Pulsed Body - Diode Forward Current				200	A
V _{SD}	Diode Forward Voltage	V _{GS} = 0 V, I _S = 25 A		0.95		V
t _{rr}	Reverse recovery time	V _{DD} = 400 V, I _D = 16 A, di/dt = 100 A/μs		130		ns
Q _{rr}	Reverse recovery charge			0.8		μC
I _{rrm}	Peak Reverse Recovery Current			11.3		A

Electrical Characteristics Diagrams



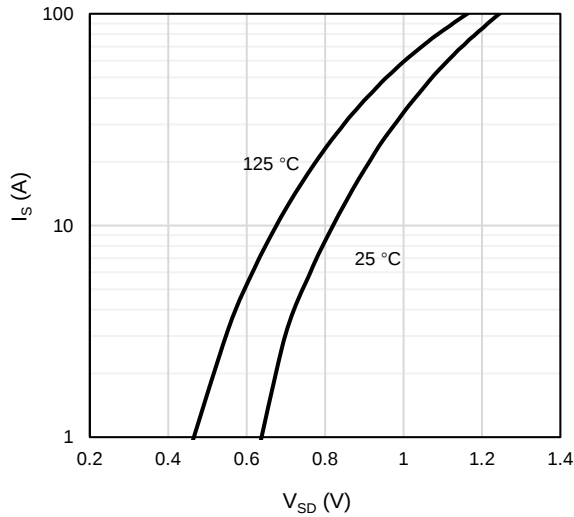


Figure 7: Body-Diode Characteristics

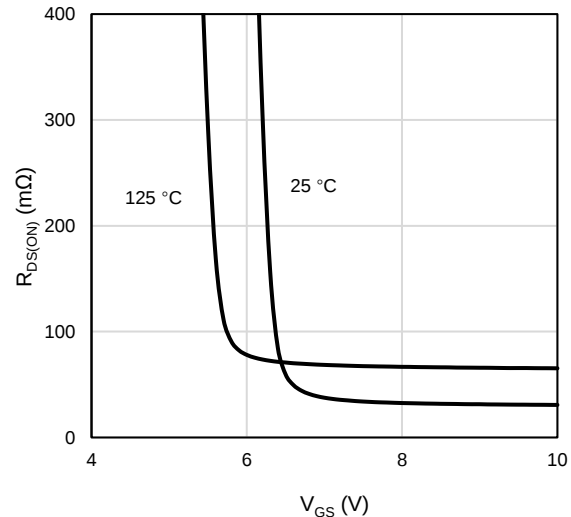


Figure 8: On-Resistance vs. Gate-Source Voltage

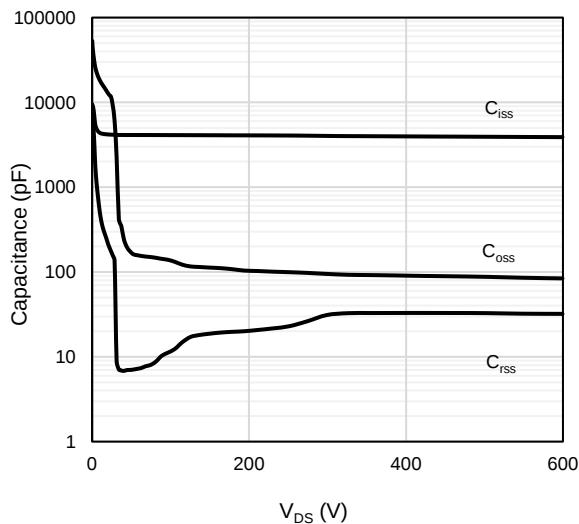


Figure 9: Capacitance Characteristics

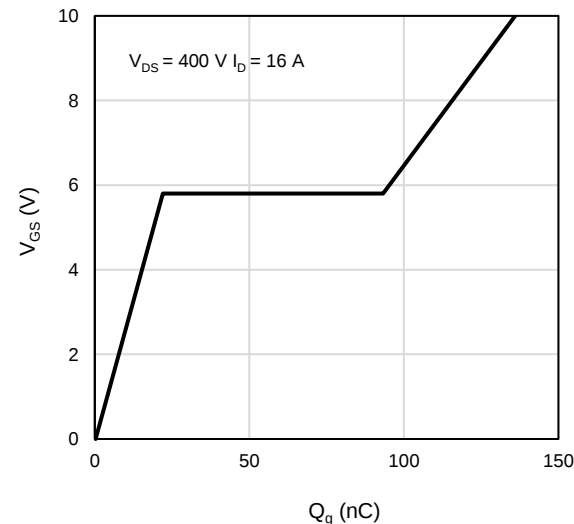


Figure 10: Gate-Charge Characteristics

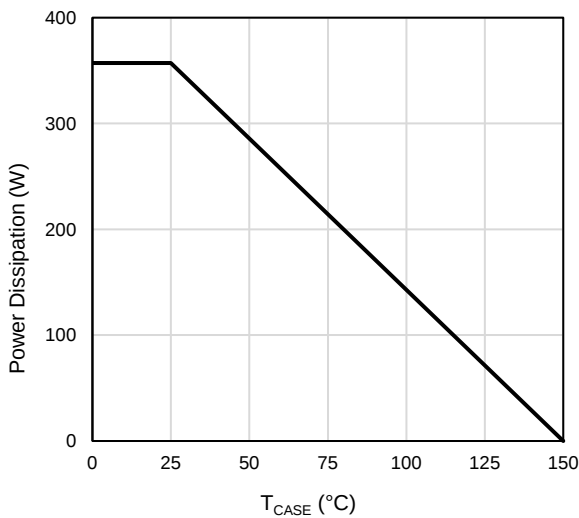


Figure 11: Power De-rating

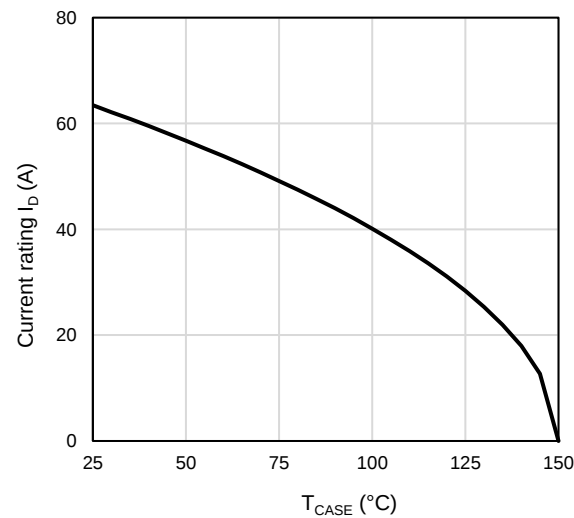


Figure 12: Current De-rating

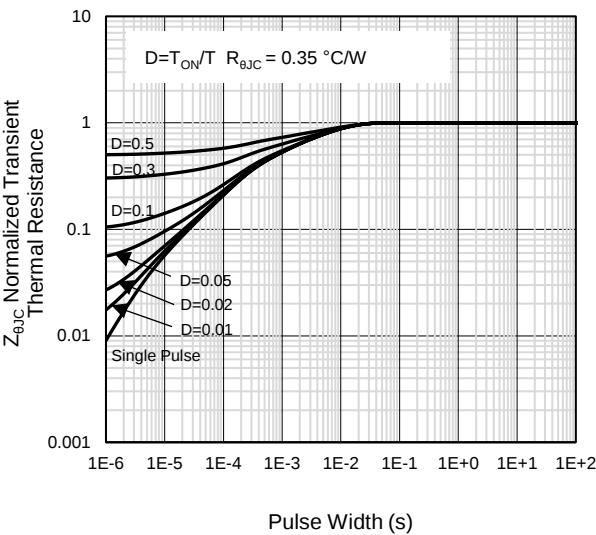


Figure 13: Normalized Maximum Transient Thermal Impedance

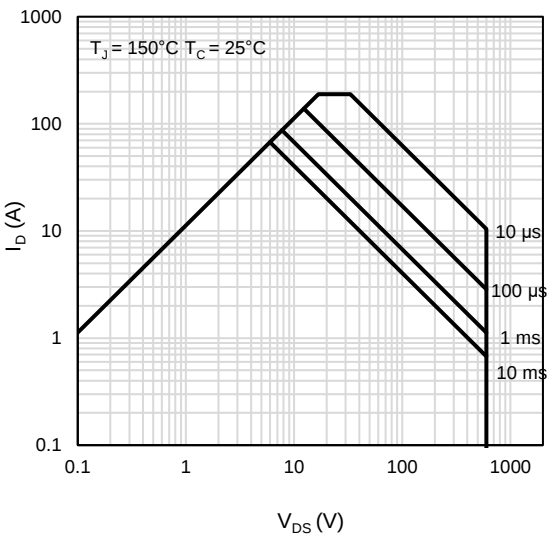
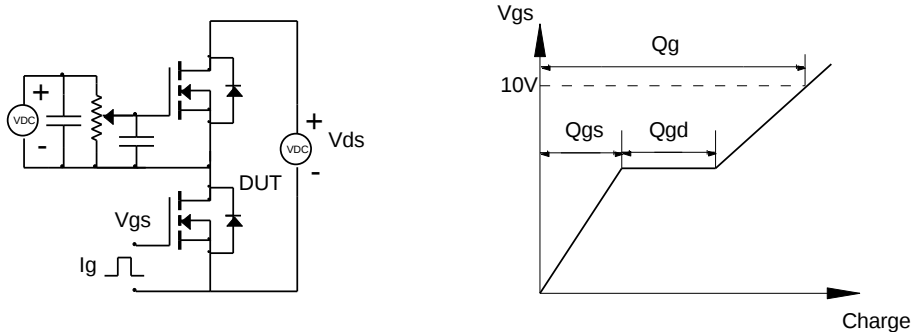


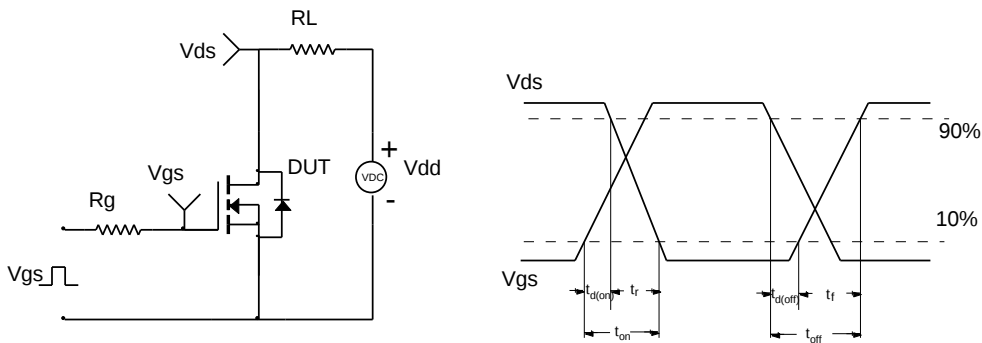
Figure 14: Maximum Forward Biased Safe Operating Area

Test Circuit and Waveform

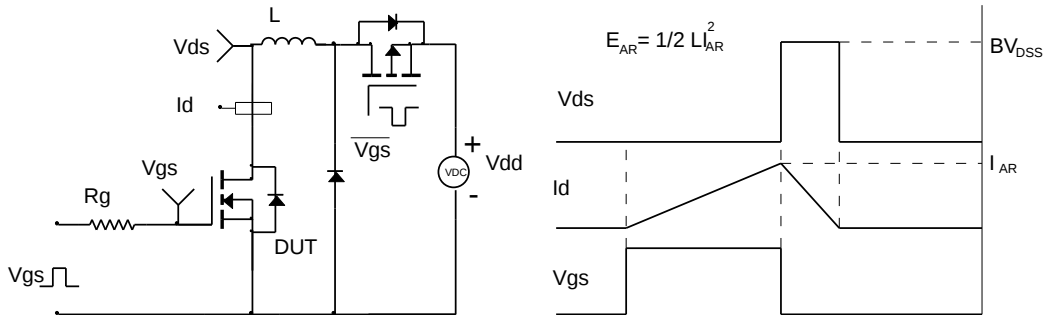
Gate Charge Test Circuit & Waveform



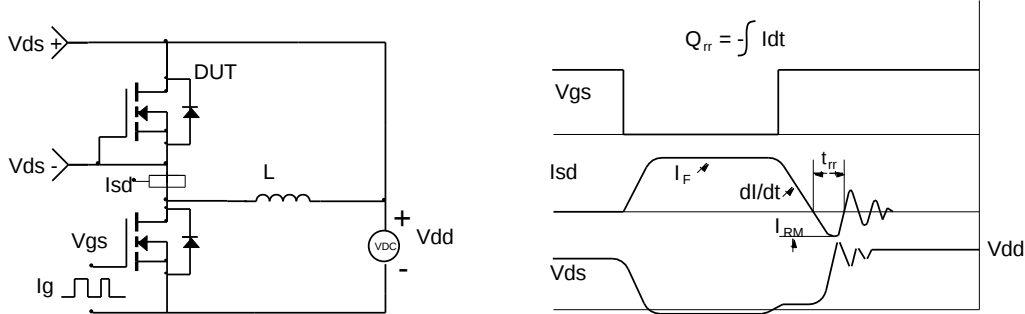
Resistive Switching Test Circuit & Waveforms



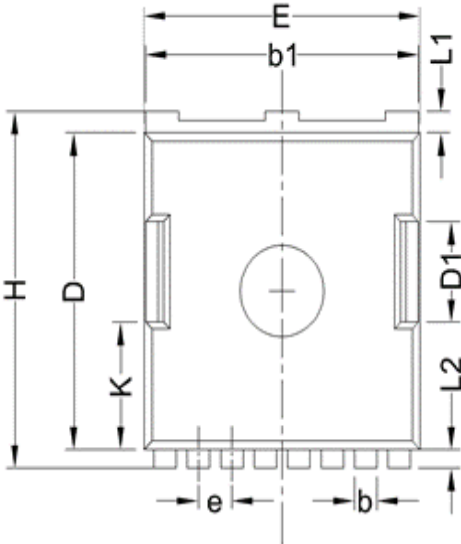
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



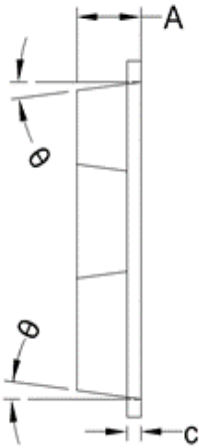
Diode Recovery Test Circuit & Waveforms



Package Outlines



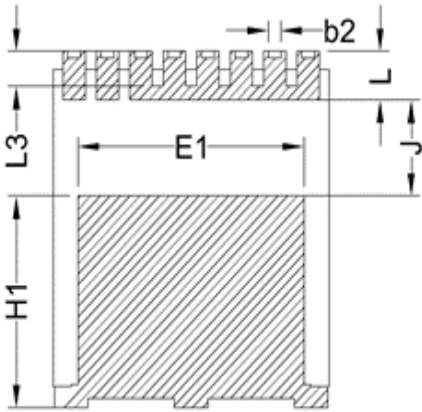
Top View



Side View



Front View



BTM View

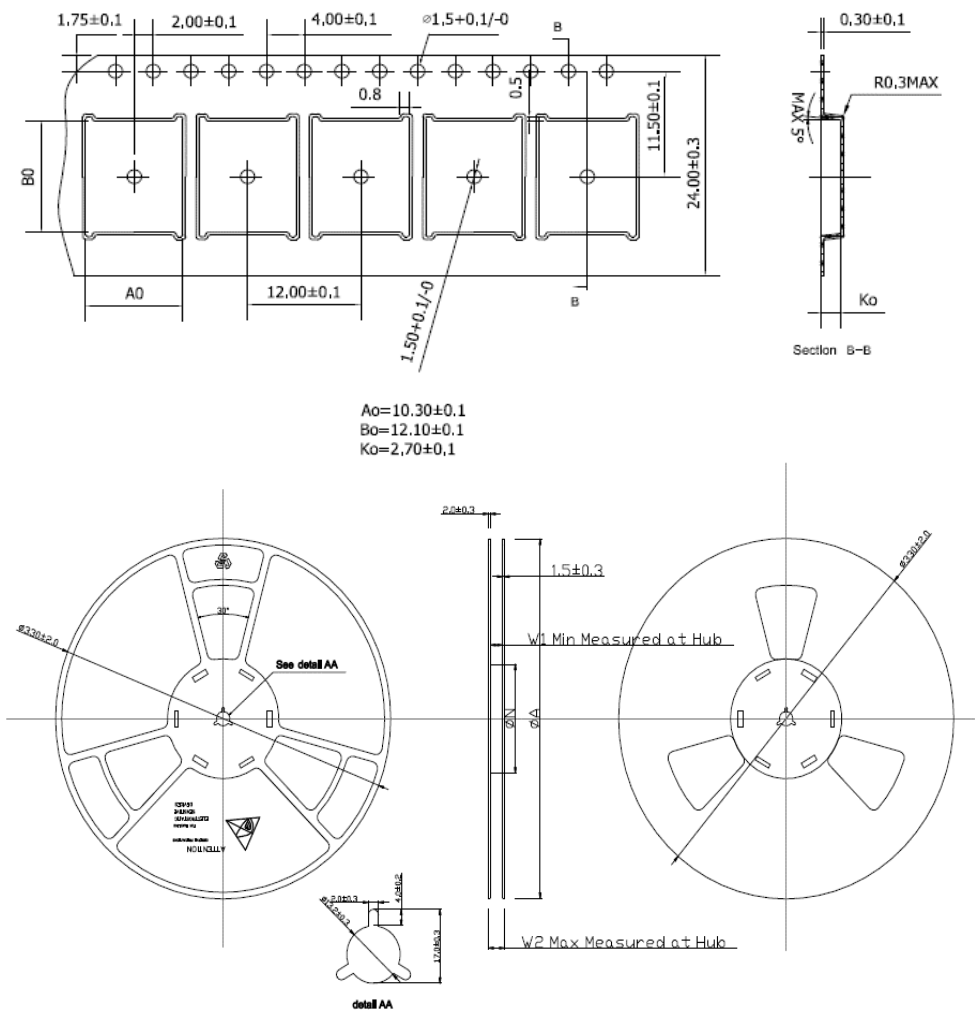
SYMBOL	MIN	NOM	MAX
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.42	0.46	0.50
c	0.40	0.50	0.60
D	10.28	10.43	10.58
D1	3.10	3.30	3.50
e	1.20 BSC		
E	9.70	9.90	10.10
E1	7.90	8.10	8.30
N	8.00		
J	3.00	3.15	3.30
K	3.98	4.18	4.38
L	1.40	1.60	1.80
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L3	1.00	1.15	1.30
θ	4°	7°	10°

Marking Information



Note:
3S60N355TMF = Product Name Code
XXXXXXX = Date code
Contact ALKAIDSEMI sales for detail information

Tape & Reel Information



Revision History

Revision	Released	Remark
Rev.1.0	2024	Initial Release

Disclaimer

The information given in this document describes the independent performance of the product, but similar performance is not guaranteed under other working conditions, and cannot be guaranteed when installed with other products or equipment. To achieve the required performance of the product in actual scenarios, the customer should conduct a complete application test to assess the functionality of the product.

Alkaidsemi assumes no responsibility for equipment failures result from using products at values that exceed the ratings, operating conditions, or other parameters listed in the product specifications.

The product described in this specification is not applicable for aerospace or other applications which requires high reliability. Customers using or selling these products for use in medical, life-saving, or life-sustaining applications do so at their own risk and agree to fully indemnify.

Due to product or technical improvements, the information described or contained herein may be changed without prior notice.